

IN THE SPECIFICATION

Please replace paragraph 1 with the following:

[0001] This application is related to copending and cofiled applications for United States Letters Patent Serial No. 09/913,983, filed July 30, 2001 and entitled SYSTEM AND METHOD FOR IN-SYSTEM PROGRAMMING THROUGH AN ON-SYSTEM JTAG BRIDGE OF PROGRAMMABLE LOGIC DEVICES ON MULTIPLE CIRCUIT BOARDS OF A SYSTEM (Attorney Docket No. 10016250-1); Serial No. 09/918,030, filed July 30, 2001 and entitled METHOD FOR ACCESSING SCAN CHAINS AND UPDATING EEPROM-RESIDENT FPGA CODE THROUGH A SYSTEM MANAGEMENT PROCESSOR AND JTAG BUS (Attorney Docket No. 10017840-1); and Serial No. 09/918,024, filed July 30, 2001 and entitled METHOD AND APPARATUS FOR SERIAL BUS TO JTAG BUS BRIDGE (Attorney Docket No. 10017841-1), all of the aforementioned applications incorporated herewith by reference thereto.